

FIG. 1

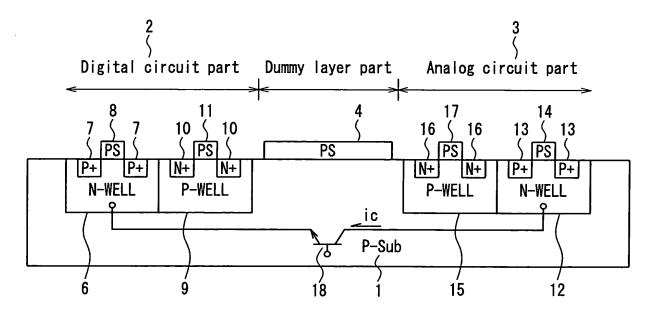


FIG. 2



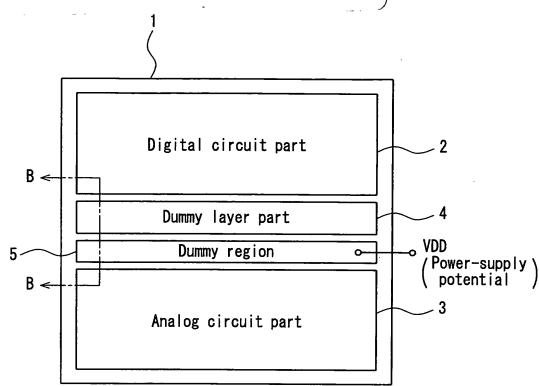


FIG. 3

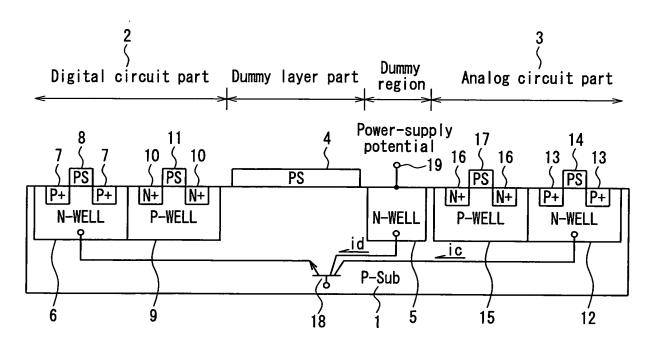


FIG. 4

Inventor: Otani
Docket No.: 10873.1453US01
Title: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE
Attorney Name: Douglas P. Mueller
Phone No.: (612) 371-5237
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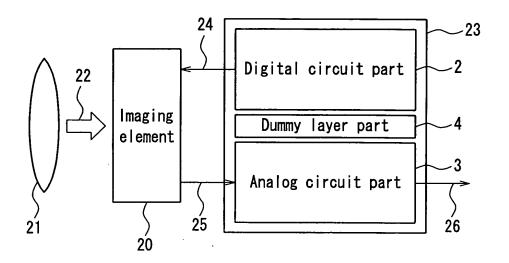


FIG. 5

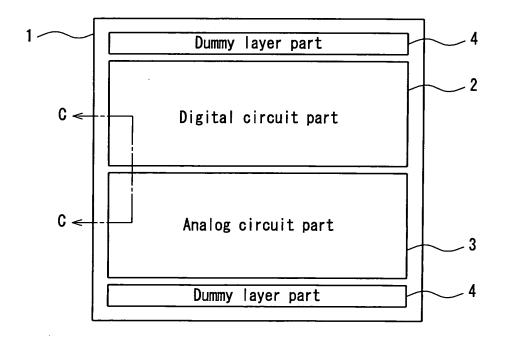


FIG. 6 PRIOR ART

Inventor: Otani Docket No.: 10873.1453US01 Title: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE Attorney Name: Douglas P. Mueller Phone No.: (612) 371-5237 Sheet 4 of 4 Digital circuit part Analog circuit part 10 \ 10 16 \ 16 13 13 N-WELL P-WELL N-WELL P-WELL _ic P-Sub 15 12 FIG. 7 PRIOR ART Dummy layer part Digital circuit part Analog circuit part

FIG. 8 PRIOR ART

Dummy layer part